

Summary of TC versus Sequences

This page is dedicated to a summary of all existing TC. The purpose is to cross check their use in one (or more) sequence using the following table.

Moreover, performed tests are reported in this table also.

In green, the TC which are in a sequence already tested.

In yellow, the TC which are in a sequence untested yet.

In red, TC which won't be in a sequence and are not tested yet.

In blue, unimplemented TC so no test is possible.

TC Name	Belongs to the sequence named :	Test ed durin g:	Using ETB version	TC Durat ion (s)	Comments
DPU_ENABL E_HK	AIWF050A				
LFR_ENABLE _HK	AIWF050B				
TDS_ENABLE _HK	AIWF050C				
THR_ENABLE _HK	AIWF050D				
DPU_S20_EN ABLE_HK	AIWF050E				
DPU_OBC_E NABLE_HK	AIWF050F	IGST 4_2	EM2	1	
DPU_DISABL E_HK	AIWF050G				Disable DPU Housekeeping Parameter Report Generation. The command allows to disable one HK report at a time: the HK report is identified by its Structure ID. Pag. 185/236 DAS UM
LFR_DISABL E_HK	AIWF050H				
TDS_DISABL E_HK	AIWF050I				
THR_DISABL E_HK	AIWF050J				
DPU_S20_DI SABLE_HK	AIWF050K				
DPU_OBC_DI SABLE_HK	AIWF050L				
DPU_UPDAT E_HK_PERIOD	AIWF032A	IGST 4_2	EM2	1	
DPU_UPDAT E_HK_PERIOD	AIWF048B				DBS HK param
DPU_OBC_U PDATE_HK_P ERIOD	AIWF032A	IGST 4_2	EM2	1	
LFR_UPDATE _HK_PERIOD	AIWF032A				
TDS_UPDAT E_HK_PERIOD	AIWF032A				
THR_UPDAT E_HK_PERIOD	AIWF032A				
DPU_S20_UP DATE_HK_PE RIOD	AIWF032A	IGST 4_2	EM2	1	
DPU_ENABL E_EVENT	AIWV001A				Enable Event Report Generation. The command allows to enable several event report. Each event is identified by its report ID. Pag. 225 DAS UM
DPU_DISABL E_EVENT	AIWV001B				Disable Event Report Generation. The command allows to disable several event report. Each event is identified by its report ID. Pag. 225 DAS UM

DPU_LOAD_MEMORY	AIWV002D	IGST 4_2	EM2	1	
LFR_LOAD_MEMORY	AIWV002A				p196 DAS UM Cannot be prepared in advance because we don't know the length of the change
TDS_LOAD_MEMORY	AIWV002B				p196 DAS UM Cannot be prepared in advance because we don't know the length of the change
THR_LOAD_MEMORY	AIWV002C				p196 DAS UM Cannot be prepared in advance because we don't know the length of the change
DPU_DUMP_MEMORY	AIWV002D	IGST 4_2	EM2	1	
LFR_DUMP_MEMORY	AIWV002A				Cannot be prepared in advance because we don't know the length of the change
TDS_DUMP_MEMORY	AIWV002B				Cannot be prepared in advance because we don't know the length of the change
THR_DUMP_MEMORY	AIWV002C				Cannot be prepared in advance because we don't know the length of the change
DPU_CHECK_MEMORY	AIWV002D	IGST 4_2	EM2	1	
LFR_CHECK_MEMORY	AIWV002A				Cannot be prepared in advance because we don't know the length of the change
TDS_CHECK_MEMORY	AIWV002B				Cannot be prepared in advance because we don't know the length of the change
THR_CHECK_MEMORY	AIWV002C				Cannot be prepared in advance because we don't know the length of the change
DPU_TEST_CONNECTION	AIWF370A	IGST 4_2	EM2	1	
DPU_ENABLE_SCIENCE	AIWV001C				Enable DPU science TM transfer. Pag. 225 DAS UM
LFR_ENABLE_SCIENCE	AIWV001C				Enable DPU science TM transfer. Pag. 225 DAS UM
DPU_PID5_ENABLE_SCIENCE	AIWV001C				Enable DPU science TM transfer. Pag. 225 DAS UM
TDS_ENABLE_SCIENCE	AIWV001C				Enable DPU science TM transfer. Pag. 225 DAS UM
DPU_PID6_ENABLE_SCIENCE	AIWV001C				Enable DPU science TM transfer. Pag. 225 DAS UM
THR_ENABLE_SCIENCE	AIWV001C				Enable DPU science TM transfer. Pag. 225 DAS UM
DPU_DISABLE_SCIENCE	AIWV001D				Disable DPU science TM transfer. Pag. 225 DAS UM
LFR_DISABLE_SCIENCE	AIWV001D				Disable DPU science TM transfer. Pag. 225 DAS UM
DPU_PID5_DISABLE_SCIENCE	AIWV001D				Disable DPU science TM transfer. Pag. 225 DAS UM
TDS_DISABLE_SCIENCE	AIWV001D				Disable DPU science TM transfer. Pag. 225 DAS UM
DPU_PID6_DISABLE_SCIENCE	AIWV001D				Disable DPU science TM transfer. Pag. 225 DAS UM
THR_DISABLE_SCIENCE	AIWV001D				Disable DPU science TM transfer. Pag. 225 DAS UM
DPU_SWITCH_OFF_EQUIPMENT	AIWF047B				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047C				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047D				Procedures ready to be tested. Switch off instrument separately.

DPU_SWITCH_OFF_EQUIPMENT	AIWF047E				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047F				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047G				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047H				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047I				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_OFF_EQUIPMENT	AIWF047K				Procedures ready to be tested. Switch off instrument separately.
DPU_SWITCH_ON_EQUIPMENT	AIWF031A	IGST 4_2	EM2	1	Switch on converter in a large procedure
DPU_SWITCH_ON_EQUIPMENT	AIWF042A	IGST 4_2	EM2	1	Switch on converter
DPU_SWITCH_ON_EQUIPMENT	AIWF042E	IGST 4_2	EM2	20	Switch on Bias
DPU_SWITCH_ON_EQUIPMENT	AIWF042F				Switch on PA. Procedure ready to be tested
DPU_SWITCH_ON_EQUIPMENT	AIWF042G				Switch on SCM. Procedure ready to be tested
DPU_RESET	AIWF046A	IGST 4_2	EM2	3	
DPU_ENABLE_WATCHDOG	AIWF011A				
DPU_DISABLE_WATCHDOG					Pag. 115 DAS UM
DPU_BOOT_DAS	AIWF030A	IGST 4_2	EM2	4	
DPU_BOOT_LFR	AIWF031A	IGST 4_2	EM2	20	
DPU_BOOT_LFR	AIWF042B	IGST 4_2	EM2	20	
DPU_BOOT_TDS	AIWF031A	IGST 4_2	EM2	20	
DPU_BOOT_TDS	AIWF042D	IGST 4_2	EM2	20	
DPU_BOOT_THR	AIWF031A	IGST 4_2	EM2	20	
DPU_BOOT_THR	AIWF042C	IGST 4_2	EM2	20	
DPU_DBS_LOAD_COMMON_PAR	AIWF048A				Pag. 169 SSS UM .
DPU_DAS_LOAD_COMMON_PAR	AIWF032B	IGST 4_2	EM2	1	
DPU_LOAD_TEMP_PAR	AIWF032E	IGST 4_2	EM2	1	
DPU_LOAD_POWER_PAR	AIWF032C	IGST 4_2	EM2	1	
DPU_LOAD_SBM1_PAR	AIWF032G	IGST 4_2	EM2	1	

DPU_LOAD_SBM2_PAR	AIWF032H	IGST 4_2	EM2	1	
DPU_DBS_DUMP_PAR	AIWF048A				Pag. 169 SSS UM .
DPU_DAS_DUMP_PAR	AIWF032A	IGST 4_2	EM2	1	
DPU_DAS_DUMP_PAR	AIWF032K	IGST 4_2	EM2	1	
DPU_ENTER_STANDBY	AIWF045A	IGST 4_2	EM2	6	
DPU_ENTER_SERVICE	AIWF031A	IGST 4_2	EM2	2	
DPU_ENTER_SERVICE	AIWF043A	IGST 4_2	EM2	2	
DPU_ENTER_SURVEY_NORMAL	AIWF035A	IGST 4_2	EM2	2	
DPU_ENTER_SURVEY_BURST	AIWF035B	IGST 4_2	EM2	2	
DPU_ENTER_SURVEY_BACKUP	AIWF044A	IGST 4_2	EM2	3	
DPU_ENTER_SBM_DETECTION	AIWF035C	IGST 4_2	EM2	3	
DPU_ENTER_SBM1_DUMP	AIWF034D				This is a Debug TC. Nominally it is run by DPU. This TC will be used to force the Dump of SBM1 data during commissioning (see SSS p2016)
DPU_ENTER_SBM2_ACQ	AIWF034E				This is a Debug TC. Nominally it is run by DPU. This TC will be used to force the Dump of SBM2 data during commissioning
DPU_ENABLE_TRANS_MODE					Not enabled during flight. It is really needed?
DPU_DISABLE_TRANS_MODE					Not enabled during flight. It is really needed?
DPU_ENABLE_COMPRESSION	AIWF041A				Procedures ready to be tested.
DPU_DISABLE_COMPRESSION	AIWF041B				Procedures ready to be tested.
DPU_ENABLE_INFO_PKT	AIWF049A				Pag. 175 DAS UM.
DPU_DISABLE_INFO_PKT	AIWF049B				Pag. 175 DAS UM.
DPU_EXECUTE_BIAS_CMD	AIWV005A				To run any generic BIAS TC Pag. 217 SSS.
DPU_SET_BIAS1	AIWF031A				
DPU_SET_BIAS1	AIWF033B				Procedures ready to be tested.
DPU_SET_BIAS1	AIWF036A				Procedures ready to be tested.
DPU_SET_BIAS1	AIWF042E				
DPU_SET_BIAS2	AIWF031A				
DPU_SET_BIAS2	AIWF033B				Procedures ready to be tested.
DPU_SET_BIAS2	AIWF036A				Procedures ready to be tested.
DPU_SET_BIAS2	AIWF042E				

DPU_SET_BI AS3	AIWF031A				
DPU_SET_BI AS3	AIWF033B				Procedures ready to be tested.
DPU_SET_BI AS3	AIWF036A				Procedures ready to be tested.
DPU_SET_BI AS3	AIWF042E				
DPU_SET_BI AS_RELAY	AIWF031A	IGST 4_2	EM2	1	
DPU_SET_BI AS_RELAY	AIWF033B				Procedures ready to be tested.
DPU_SET_BI AS_RELAY	AIWF033D				Procedures ready to be tested.
DPU_SET_BI AS_RELAY	AIWF040A				Procedures ready to be tested.
DPU_SET_BI AS_RELAY	AIWF042E	IGST 4_2	EM2	1	
DPU_SET_BI AS_RFREQ	AIWV005B				Not implemented in our commands probably needed for calibration (mail Vaivads 23 mar and 3 May 2017). An example to test the TC is in email JMT 15/02/2018 attachment SFT CNES v3.3.xml. A voir avec l'équipe BIAS
DPU_SET_BI AS_SWEEP	AIWV005C				This procedure execute the BIAS sweep. We use the command start bias sweep to perform the Sweep. An example to test the TC is in email JMT 15/02/2018 attachment SFT CNES v3.3.xml. Should be tested independently (not in a sequence)
DPU_SET_BI AS_MODE	AIWF031A				
DPU_SET_BI AS_MODE	AIWF033B				Procedures ready to be tested.
DPU_SET_BI AS_MODE	AIWF033D				Procedures ready to be tested.
DPU_SET_BI AS_MODE	AIWF040A				Procedures ready to be tested.
DPU_SET_BI AS_MODE	AIWF042E				
DPU_SET_BI AS_MODE	AIWF047A				Procedures ready to be tested.
DPU_SET_BI AS_PAGE	AIWV005D				Selects the accessed register page. An example to test the TC is in email JMT 15/02/2018 attachment SFT CNES v3.3.xml A voir avec l'équipe BIAS
DPU_EXECU TE_PDU_CMD	AIWV006A				Generic telecommand controlling the PDU. An example to test the TC is in email JMT 15/02/2018 attachment SFT CNES v3.3.xml
DPU_START_ FROM_CONT EXT					According to the DAS UM this TC is not implemented. However, an example to test the TC is in email JMT 15/02/2018 attachment SFT CNES v3.3.xml Essayer de le jouer au sol mais pas sûr que ça marche
DPU_LOAD_ BIAS_SWEEP	AIWF033D				Procedures ready to be tested.
DPU_START_ BIAS_SWEEP	AIWF033D				Procedures ready to be tested.
DPU_START_ BIAS_CALIB	AIWF033B				Bias calibration has been performed by sequentially reproducing the calibration steps. Procedures ready to be tested once validated by Bias team?
DPU_RESET_ TM_BUFFER	AIWV004B				
DPU_CLEAR_ HK	AIWF032J	IGST 4_2	EM2	1	
DPU_START_ TEST					DAS UM pag. 297. Only for test purpose on ground. Shall not be include in a sequence
DPU_STOP_T EST					DAS UM pag. 298. Only for test purpose on ground. Shall not be include in a sequence
DPU_LOAD_ WAVEFORM_ PAR	AIWF032F	IGST 4_2	EM2	1	
DPU_LOAD_ SUN_DISTAN CE	AIWV004A				

DPU_LOAD_BHV_PAR	AIWF032D				Procedures ready to be tested.
DPU_CLEAR_BIAS_SWEEP	AIWF033B-D				DAS UM pag. 261. Is it needed prior to load a new sweeping table
DPU_SAVE_CONTEXT					According with the DAS UM this TC is not implemented. However, an example to test the TC is in email JMT 15/02/2018 attachment SFT CNES v3.3.xml Essayer de le jouer au sol mais pas sûr que ça marche
DPU_LOAD_VSC_PAR	AIWF032I	IGST 4_2	EM2	1	
DPU_LOAD_RWF_PAR	AIWF051A				DAS UM pag. 182. Need a specific procedure to disabled reaction wheel frequencies filtering for LFR interference campaign
LFR_RESET	AIWV003A				LFR UM p23 : shall not be used. Shall be tested not in a sequence.
TDS_RESET	AIWV003A				No information in TDS UM ... probably the same than for LFR
THR_RESET	AIWV003A				No information in THR UM ... probably the same than for LFR
LFR_LOAD_COMMON_PAR	AIWF033D	IGST 4_2	EM2	1	
LFR_LOAD_COMMON_PAR	AIWF039C	IGST 4_2	EM2	1	
TDS_LOAD_COMMON_PAR	AIWF038A	IGST 4_2	EM2	1	
LFR_LOAD_NORMAL_PAR	AIWF039D	IGST 4_2	EM2	1	
TDS_LOAD_NORMAL_PAR	AIWF038E	IGST 4_2	EM2	1	
THR_LOAD_NORMAL_PAR_1	AIWF037A	IGST 4_2	EM2	1	
THR_LOAD_NORMAL_PAR_2	AIWV007A				As we don't know how many frequencies we will put in the TC we cannot prepare the sequence in advance.
THR_LOAD_NORMAL_PAR_3	AIWV007B				As we don't know how many frequencies we will put in the TC we cannot prepare the sequence in advance.
LFR_LOAD_BURST_PAR	AIWF039E	IGST 4_2	EM2	1	
TDS_LOAD_BURST_PAR	AIWF038F	IGST 4_2	EM2	1	
THR_LOAD_BURST_PAR_1	AIWF037B	IGST 4_2	EM2	1	
THR_LOAD_BURST_PAR_2	AIWV007C				As we don't know how many frequencies we will put in the TC we cannot prepare the sequence in advance.
THR_LOAD_BURST_PAR_3	AIWV007D				As we don't know how many frequencies we will put in the TC we cannot prepare the sequence in advance.
LFR_LOAD_SBM1_PAR	AIWF039A	IGST 4_2	EM2	1	
TDS_LOAD_SBM1_PAR	AIWF038B	IGST 4_2	EM2	1	
LFR_LOAD_SBM2_PAR	AIWF039B	IGST 4_2	EM2	1	
TDS_LOAD_SBM2_PAR	AIWF038C	IGST 4_2	EM2	1	
THR_LOAD_CALIBRATION_PAR	AIWF037C	IGST 4_2	EM2	1	
LFR_DUMP_PAR	AIWF034A	IGST 4_2	EM2	1	
TDS_DUMP_PAR	AIWF034A	IGST 4_2	EM2	1	
THR_DUMP_PAR	AIWF034A	IGST 4_2	EM2	1	
LFR_ENTER_MODE					TC executed when TC_DPU_ENTER_MODE is sent.. No need for a specific sequence. Shall be tested independently.

TDS_ENTER_MODE					TC executed when TC_DPU_ENTER_MODE is sent.. No need for a specific sequence. Shall be tested independently.
THR_ENTER_MODE					TC executed when TC_DPU_ENTER_MODE is sent.. No need for a specific sequence. Shall be tested independently.
LFR_ENABLE_CALIBRATION	AIWF033A				Procedure ready to be tested
THR_ENABLE_CALIBRATION	AIWF033C				Procedure ready to be tested
LFR_DISABLE_CALIBRATION	AIWF033A				Procedure ready to be tested
TDS_DUMP_NORMAL_TSWF	AIWF034B				Check the sequence before testing
TDS_DUMP_SBM2_TSWF	AIWF034C				no need for FP
TDS_PERFORM_DIAGNOSIS	AIWF059A				TDS UM : p23
TDS_LOAD_LFM_PAR	AIWF038D	IGST 4_2	EM2	1	
LFR_LOAD_FBINS_MASK	AIWF051C				LFR UM : p22
LFR_LOAD_KCOEFFICIENTS	AIWF051B				TC to be run during the i inter calibration phase Quels paramètres?
LFR_DUMP_KCOEFFICIENTS	AIWF034E				TC to be run during the i inter calibration phase Need a TM check ?
LFR_LOAD_FILTER_PAR	AIWF051D				TC to be run during the i inter calibration phase - See LFR RPW 6 doc config